

## JOHN F. GUAY

### Experience

**Potomac Patent Group PLLC**, Washington, D.C. (July 2007 - present) - *Patent Agent*

- Draft, file and prosecute patent applications related to communications systems, with particular emphasis on telecommunication systems.

**Nixon Peabody LLP**, Washington, D.C. (2005-2007) - *Patent Agent*

- Work performed similar to BDSM, below, but with increased decision-making responsibilities and ongoing junior associate training and review.
- Advised patent professionals regarding Patent Office procedures.

**Burns Doane Swecker & Mathis LLP**, Alexandria, VA (2001-2005) - *Patent Agent*

- Prepared patent and PCT applications, and prosecuted applications before the United States Patent and Trademark Office, instructed foreign associates on matters concerning counterpart U.S. applications, prepared appeal briefs, conducted oral arguments, prepared and prosecuted reexamination and reissue applications, and assisted attorneys in preparing opinions and other matters.
- Technologies covered: semiconductor devices and fabrication methods, MEMS, NEMS and other nano-sized devices, fiber optics, optical disc systems, computer-related organization of data and processing of data, computer hardware and software, digital and analog communication and tracking systems (e.g., radar, GPS-based guidance systems, and cellular systems), codecs (e.g., encryption, MPEG-4 coding), and other electrical, electronic, and electro-mechanical arts. Work also included preparing and prosecuting business-related patent applications.

**Morgan Lewis & Bockius LLP**, Washington, D.C. (2000-2001) - *Technical Specialist*

- Drafted, filed and prosecuted patent applications related to optical modulators, MEMS optical cross-connect switches, liquid crystal displays, and business methods.

**United States Patent and Trademark Office**, Arlington, VA (1993-2000) - *Patent Examiner*

- Examined patent and PCT applications relating to semiconductor devices, their use in integrated circuit technology, and methods of fabricating semiconductor devices.

### Education

B.S.E.E., University of Maryland, 1992

### Admissions

United States Patent Office, Registration No. 47,248